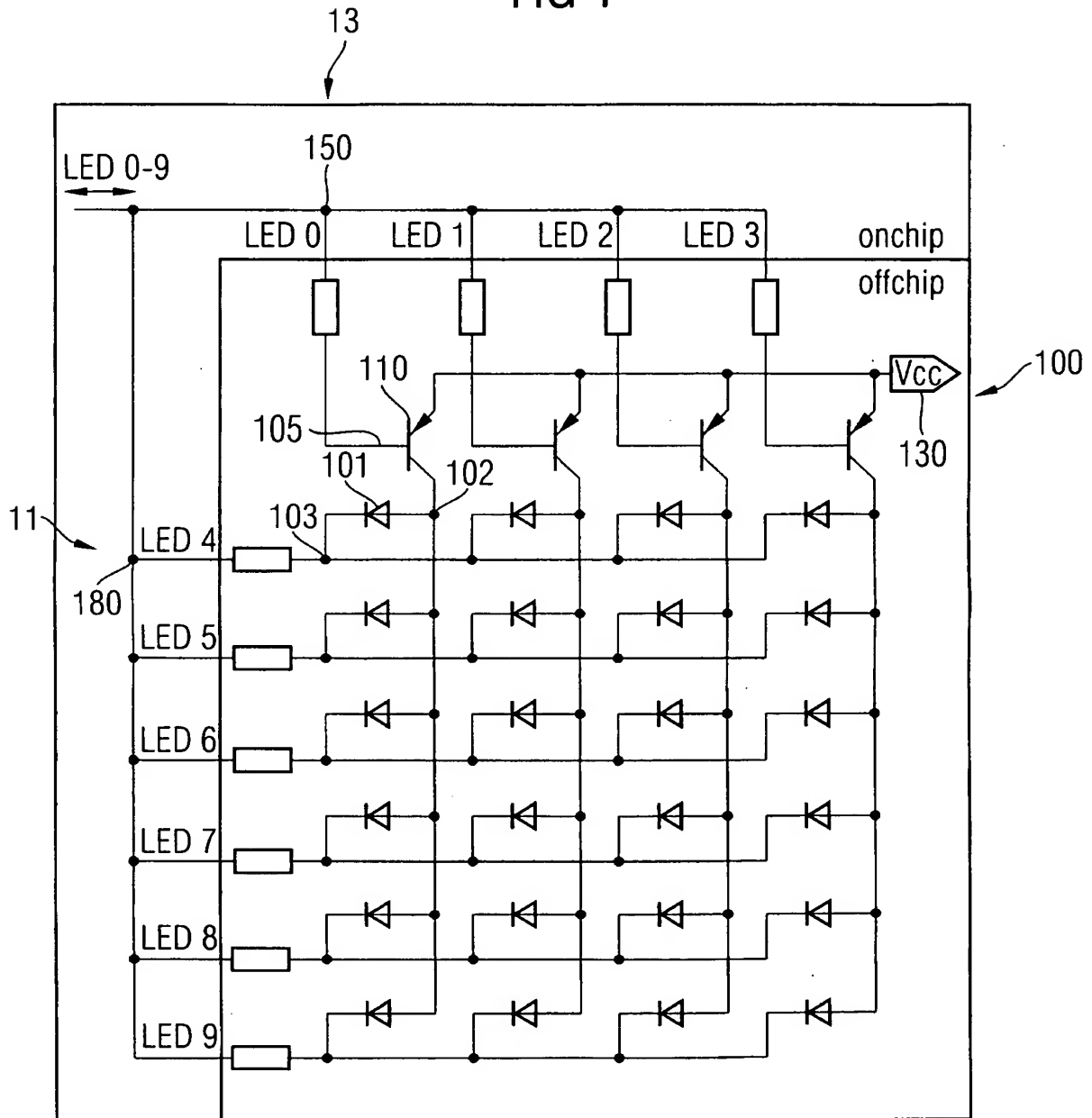


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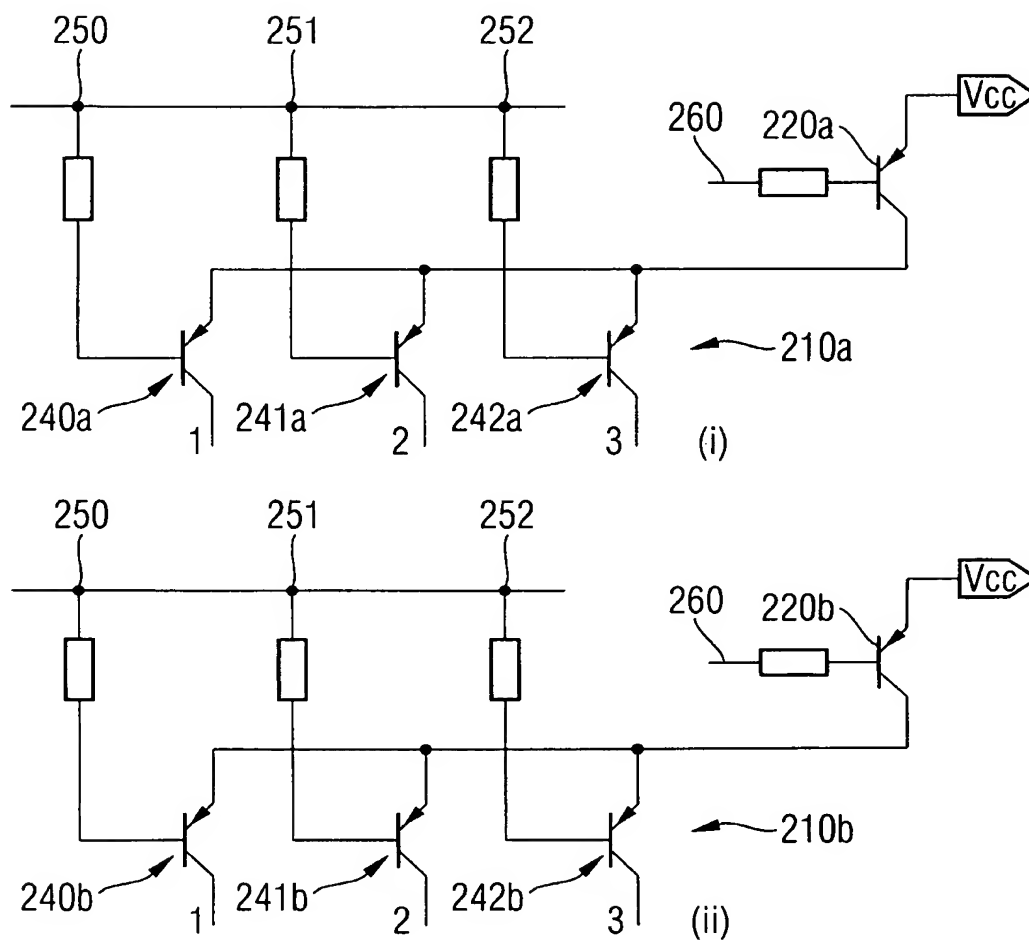
FIG 1



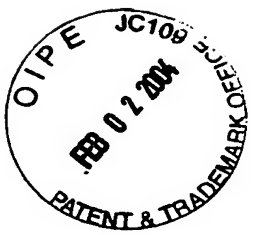
LED matrix as implemented in INCA-IP

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FIG 2

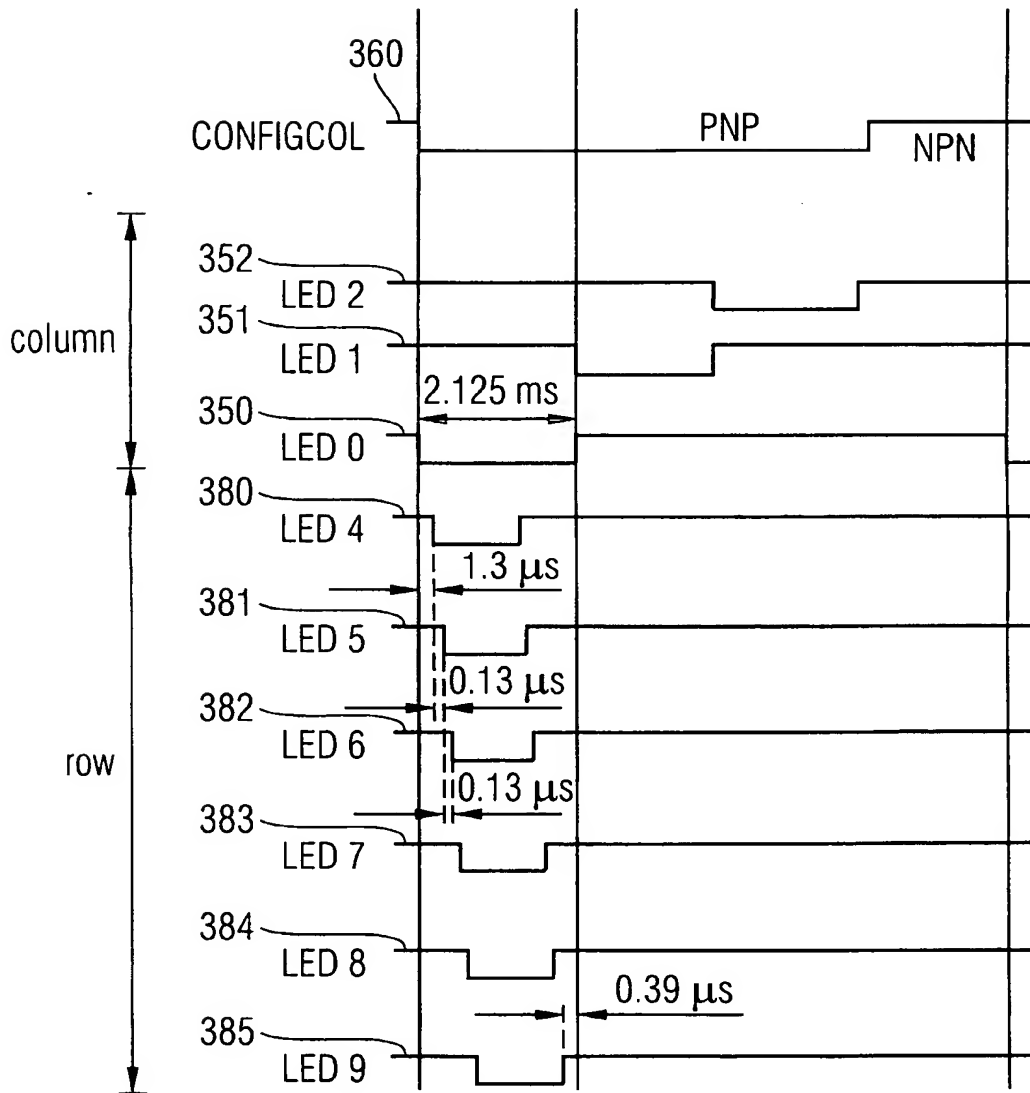


Row Decoding for output lines 1-4



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FIG 3

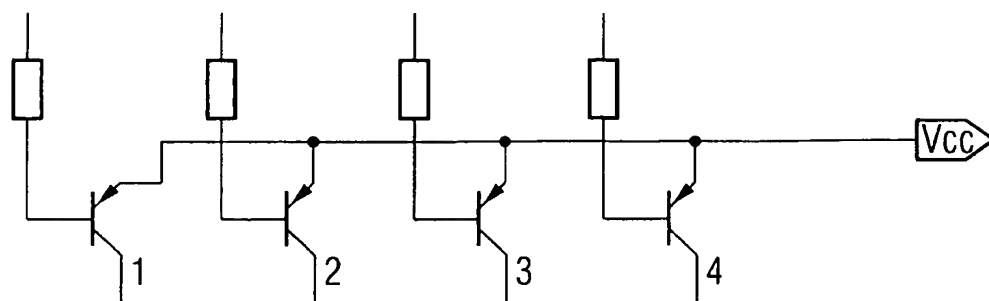


Timing Diagram of signals on LED output lines

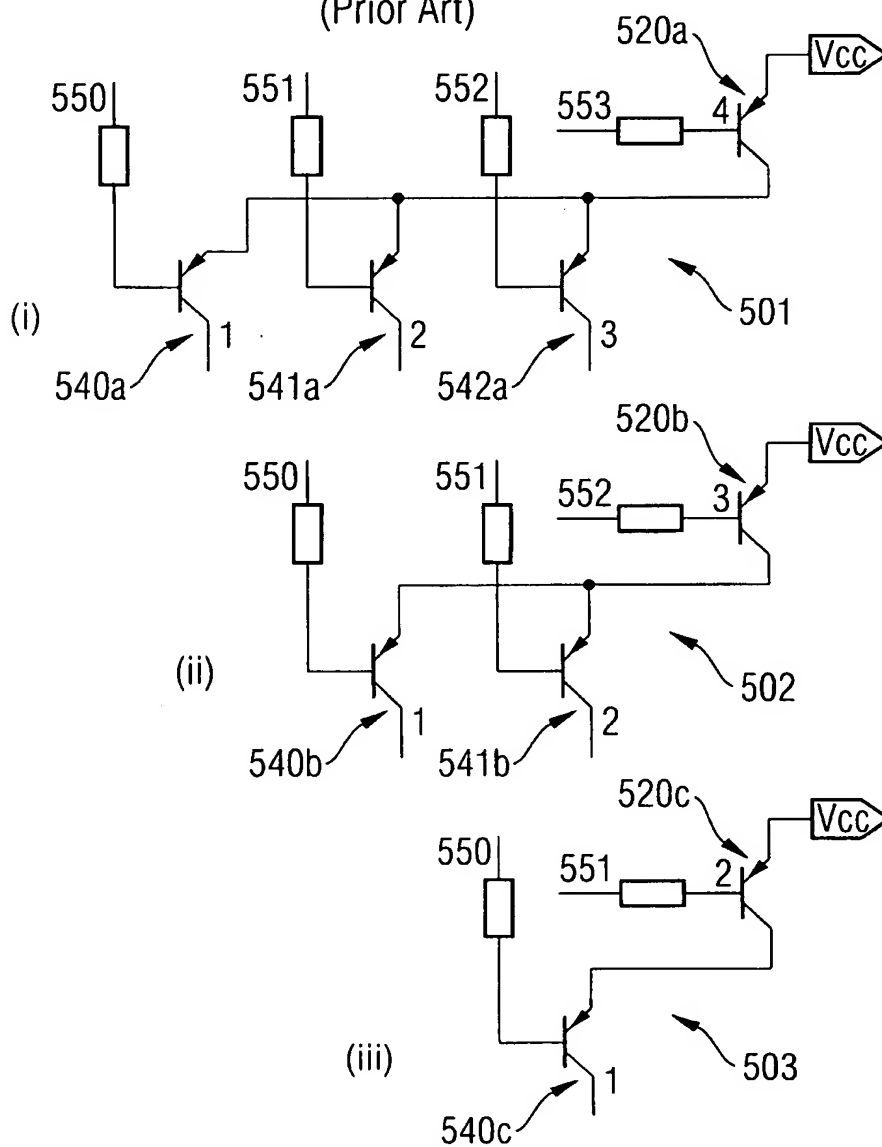


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Full-Decoding

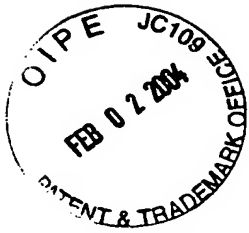


(Prior Art)



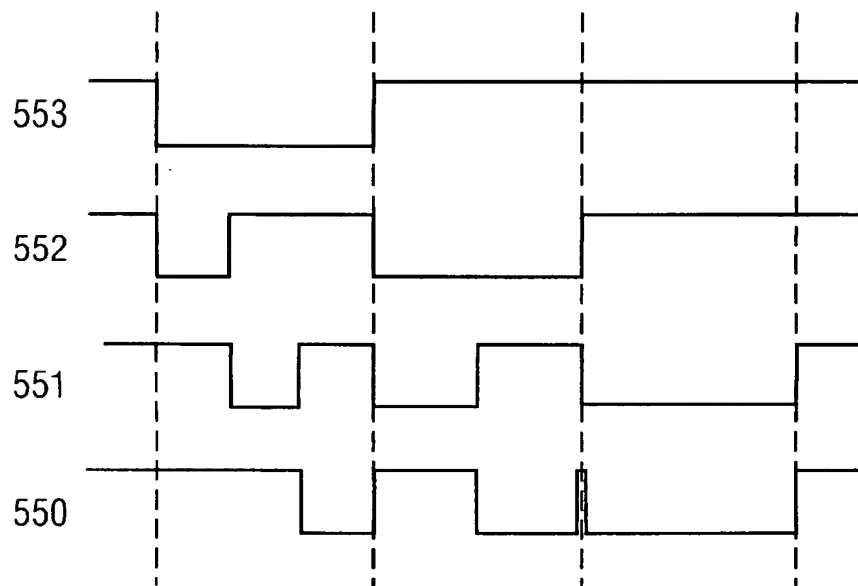
Decoding technique using all the column control input

FIG 5



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FIG 6



Timing Diagram with full-decoding